Application Engineer - Digital Design & Functional Verification

Apply Now

Company: Siemens Electronic Design Automation Ltd

Location: Herzliyya

Category: other-general

About us

Siemens Digital Industries Software - Transform the everyday

Let's make the difference together!

Meet the team -

Siemens EDA is a global technology leader in Electronic Design Automation software. Our software tools enable companies around the world to develop highly innovative electronic products faster and more cost-effectively. Our customers use our tools to push the boundaries of technology and physics to deliver better products in the increasingly complex world of chip, board, and system design.

About the Role

We are looking for an Application Engineer to focus on Digital Verification and Design.

As our Application Engineer (AE), you'll play a pivotal role as the bridge between Siemens EDA and our customers. This dynamic and challenging position demands profound technical expertise, robust communications, and exemplary business skills. Collaborating with businesses spanning diverse technology sectors, you'll assist our customers in tackling technical and business challenges using Siemens EDA's cutting-edge verification solutions. As part of our technical sales team, you'll contribute to the growth of Siemens EDA's functional verification business in Europe by ensuring the successful adoption and expansion of our products and services.

This position is based in Israel, hybrid working is available, but you will need to visit our office in Herzliya, as well as visit customer sites across Israel.

Who You Are

Driving business for Siemens EDA using hands-on technical expertise. This requires working directly with customers as a part of an account team.

Providing customer verification methodology guidance, verification tools training, and verification problem identification & resolution.

Building & maintaining relationships with customers, understanding their technical needs & challenges.

Managing multiple customer issues concurrently, prioritizing activities as required. Work with design & verification engineers as well as their level managers.

Providing technical feedback to Siemens EDA product development teams relating to critical issues and recommended product enhancements.

What You've Done

A degree in Electrical Engineering (or equivalent) with commercial experience in design & functional verification.

A strong understanding of design and verification concepts in ASIC & FPGA flows.

Experience with simulation and debug tools, with understanding of how to configure & optimize tools for maximum efficiency.

Verification planning and management experience with block level and top-level designs.

Experience with HDL coding (VHDL / Verilog / System Verilog) and UVM.

Expertise in clock/reset and low power design & verification techniques.

Experience with Linux and Windows environments including scripting languages.

Excellent presentation and communication skills in Hebrew & English

Willingness to travel (domestically and internationally) for customer meetings and training.

We are Siemens

A collection of over 377,000 minds building the future, one day at a time in over 200 countries. We're dedicated to equality, and we welcome applications that reflect the

diversity of the communities we work in. All employment decisions at Siemens are based on qualifications, merit, and business need. Bring your curiosity and creativity and help us shape tomorrow!

We offer a comprehensive reward package which includes a competitive basic salary, bonus scheme, generous holiday allowance, pension, and private healthcare.

Siemens. Making real what matters

If you want to make a difference – make it with us!

#LI-IG1

#LI-EDA

#LI-Hybrid

#HDL

#VHDL

#Verilog

#UVM

#ASIC

#FPGA

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