Design Verification Engineer - Camera

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Company: Apple

Location: Israel

Category: other-general

Summary:

Imagine what you could do here. At Apple, new ideas have a way of becoming great products, services, and customer experiences very quickly. Bring passion and dedication to your job and there's no telling what you could accomplish.The people who work here have reinvented entire industries with all Apple Hardware products. The same passion for innovation that goes into our products also applies to our practices - strengthening our dedication to leave the world better than we found it. Join us to help deliver the next groundbreaking Apple products. The Pixel DV group works on complex, large-scale media IPs. We partner with our architecture and design teams to deliver high quality media IP and systems in order to meet the demanding requirements for excellent products, while boosting performance and meeting area and power goals, incorporating new high quality features.

Key Qualifications:

+2 years experience in SoC Verification, media IP verification experience is a plusYou will need to have advanced knowledge of SoC/Media IP architecture/design & in-depth knowledge of verification flow.Expected to have a deep understanding and shown experience in advanced verification process, including dynamic and coverage based.Familiarity with verification environments, UVM, SystemVerilog – an advantage.Knowledge of hardware acceleration – an advantage.Scripting and programming experience using several of the following: Python, Perl, e, SystemVerilog, C++, and TCL

Description:

You will develop verification test plans, test benches, tools and infrastructure, protocol

monitors and agents, and coverage driven stimulus.Apply advanced techniques to achieve verification with the highest quality, productivity, and time-to-market.Apply deep system level understanding to find system architecture bugs, verifying the DUT at multiple levels - from block level to the entire IP and subsystem, with additional emphasis on power (NLP) and performance. You will work closely with the design and architecture teams from the early stages of feature definition, to ensure timely delivery of quality designs. Working with methods to accelerate verification time.Involvement with Post Silicon Validation and other verification teams.Candidate should have good interpersonal and collaboration skills, to work with our partner Design and Architecture teams

Additional Requirements:

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