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Real-time embedded Firmware Engineer

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We are looking for a Real-time embedded and DSP engineer to work on a state-of-the-art 5G product. As an engineer on the Firmware team, you will be at the center of the embedded 5G firmware development within a silicon design group responsible for designing a state-of-the-art SoC.

The position requires someone capable of developing real-time embedded control and signal processing firmware on a complex SoC platform – implementing algorithmic specifications on the target platform, managing all aspects of embedded software development and real-time control, integrating into the full system and validating against simulation models.

RESPONSIBILITIES

Implement key 3GPP NR/LTE standard DSP algorithms with hard real-time constraints, tight latency and energy budgets on custom ASIC HW

Work closely with the Algorithm and Systems engineering team to refine signal processing and control algorithms for efficient firmware/hardware execution

Work with the ASIC designers to define HW interfaces, processors, bus, DMA, accelerator, and memory subsystems

Work closely with the SW Architect to specify, design, and implement the firmware

architecture of a state-of-the-art wireless communications system

Assist in the development of pre-silicon simulation system and use it for pre-silicon FW design and HW verification

Evaluate and implement tools to support system build, formal verification and test

KEY EXPERIENCE AND SKILLS REQUIREMENTS

At least 5 years of experience implementing real-time embedded firmware and signal processing algorithms on a deeply embedded CPU or DSP HW – (Experience with CEVA cores DSPs / Control CPUs an advantage)

Deep understanding of software engineering principles, and core computer science fundamentals

Proficiency with C and source code control tools (GIT advantage)

Experience with Real Time Operating Systems (experience with FreeRTOS an advantage)

Working knowledge of computer architecture with particular emphasis on the HW/SW interfaces of high speed communication subsystems

Proficiency at debugging DSP algorithms FW and embedded software systems

Experience with simulation and software modeling – an advantage

Experience in GIT and SVN version-control systems – an advantage

Understanding of digital communications and signal processing principles – an advantage

Experience with NR / LTE 3GPP protocols and standards – an advantage

PROFILE

Degree in Electrical Engineering and/or Computer Science from a leading university in Israel or overseas. Preference to candidates that studied both disciplines (Computer Engineering or Electrical Engineering and Comp. Science)

At least 5 years of experience in in development of hard real-time systems

Good understanding of hardware and VLSI designs

System-level knowledge

Ability to work independently with minimal supervision and to lead development tasks

Ability to work under aggressive schedules for extended periods, multi-disciplinary, embrace responsibilities and challenges, success driven

Team player and good communication skills

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