

SOC technical lead

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Company: Jobinfo

Location: Ramat Gan

Category: other-general

Job Description:

For a startup that is reimagining high-performance computing, with offices in the center and in the North

Additional Positions:

Location:

Northern Israel, Haifa area, Tel Aviv - Center

Category:

Job Qualifications:

5+ years of experience in complex ASIC designs

VLSI expert with a deep understanding of chip architecture and design flows

Excellent leadership and interpersonal skills, able to drive colleagues to achieve the project goals

Good understanding of hardware-software partitions and tradeoffs

Understanding of chip integration, including I/O, high speed IPs, and PLLs

Experience in and a good understanding of synthesis, timing tools and flows

Good understanding of reset and clock design (including CTS)

Knowledge of process nodes and place and route flows

Experience working with various front-end tools and flows (CDC, LINT, etc.)

Knowledge in DFT (MBist, ATPG): advantage

Knowledge in high speed design: advantage

Company Occupation:

High Tech

Company Size:

Large (150+)

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